

Customer No.: 31561
Docket No.: 12194-US-PA
Application No.: 10/707,738

REMARKS

This is a full and timely response to the outstanding non-final Office Action mailed Dec. 14, 2005. Applicant submits that a typing informality has been corrected as instructed by the Examiner, and new claims 16-21 are added hereby. The subject matters of the newly added claims 16-21 can be found from original claims 10-15 and thus no new ground can be necessitated thereby and thus applicants submit that the next Office Action should not be made Final in accordance with the minor amendment of claim 1 for better readability and the newly added claims 16-21. Reconsideration and allowance of the application and presently pending claims 1-7, as originally filed, are respectfully requested.

Claim Objection

Claims 1-9 are objected to because of a space between the words "A" and "device" is missed. In response to the objection, Applicants hereby submit that only claim 1 has such informality, and a correction according to the instruction of the Examiner has been made. As such, claims 1-9 are in their proper form for allowance.

Claim Rejections – 35 U.S.C. § 103

The Office Action rejected claims 1-3, 6-9 under 35 U.S.C. 103(a) as being unpatentable over Kuan US 2002/0109675. The Office Action also rejected claims 1-3 and 5-15 under 35 U.S.C. 103(a) as being unpatentable over Chung US 2002/0167470.

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In response to the rejection to claims 1-3 and 6-9 under 35 U.S.C. 103(a) as being unpatentable over Kuan US 2002/0109675, Applicants hereby otherwise traverse this rejection. As such, Applicant submits that claims 1-3 and 6-9 are now in condition for allowance.

With respect to claim 1, as originally filed, recites in part:

Claim 1 A device capable of integrating a card-reading function and an instruction-input function, having a printed circuit board (PCB) and a transmission interface on the printed circuit board for coupling to an external device, comprising:

a memory card ... and
an integrated chip electrically coupling to the transmission interface, the memory card connector and the human-machine interface module, wherein the integrated chip is capable of parallel processing input/output of the memory card connector and transmitting the break instruction from the human-machine interface module to the external device.

Applicants submit that such a device as set forth in claim 1 is neither taught, disclosed, nor suggested by Kuan, Chung or any of the other cited references, taken alone or in combination.

Kuan fails to disclose, teach or suggest "a device capable of **integrating a card-reading function and an instruction-input function**" as set forth in claim 1 (Emphasis added). Kuan teaches that a USB card reading device 20 is arranged in the casing for reading the memory card 30 received in the slot 10 (Paragraph 0014). Kuan also does not teach "an integrated chip electrically coupling to the ... the memory card connector" (Emphasis added). What can be learned from Kuan is the USB controller 40 that is alleged to be a integrated

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chip by the Examiner is connected to the USB-based memory card reading device 20 for transmission of signals from ... the card reading device 20 to the computer system 60 (Paragraph 0015). Thus it is further supported that the card-reading function is not integrated in the integrated chip and therefore the card-reading function is separately performed from the instruction-input function, rather than integrated. It also teaches that only output of the memory card is processed thereby, which is different from the claimed input/output of the memory card connector processed by the integrated chip. Therefore, claim 1 as currently amended should not be considered as being anticipated by Kuan or any of the other cited references, taken alone or in combination.

In respond to the rejections to claims 1-3 and 5-15 under 35 U.S.C. 103(a) as being unpatentable over Chung US 2002/0167470. Applicants hereby otherwise traverse the rejection. As such, Applicants submit that 1-3 and 5-15 is new and nonobvious over Kuan, Chung, or any of the other cited references, taken alone or in combination, and thus should be allowed.

Chung fails to disclose, teach or suggest "a device capable of **integrating a card-reading function and an instruction-input function**" as set forth in claim 1 (Emphasis added). Chung teaches "the data processor 13 is integrally connected to the I/O interface 12 so as to read data in the memory card 2 (Paragraph 0016)" and further teaches "the coordinate output circuit 14 is connected to the microcontroller 11 for outputting a moving coordinate or a selection signal selected or made through the mouse 1 (Paragraph 0017)". Considering the Examiner assumed the data processor 13 for performing the card-reading function, and the

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coordinate output circuit 14 for performing the instruction-input function, as also shown in FIG. 5, they are not integrated.

With respect to claim 10, Applicants submit that the Examiner fails to particularly indicate each of all elements taught by Chung. Chung does not teach an integrated chip comprising an interface engine...; a common input/output module...; a memory module...; and a micro-controller, as set forth in claim 10. Accordingly, the present invention as set forth in claim 10 is new and nonobvious over Chung, or any of the other cited references, taken alone or in combination, and thus should be allowed.

If independent claim 1 is allowable over the prior art of record, then its dependent claims 2-9 are allowable as a matter of law, because these dependent claims contain all features of their respective independent claim 1. *In re Fine*, 837 F.2d 1071 (Fed. Cir. 1988).

If independent claim 10 is allowable over the prior art of record, then its dependent claims 11-15 are allowable as a matter of law, because these dependent claims contain all features of their respective independent claim 10. *In re Fine*, 837 F.2d 1071 (Fed. Cir. 1988).

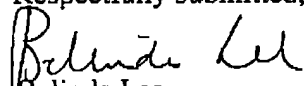
Newly added claims 16-21 depend from independent claim 1, thus are also allowable.

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CONCLUSION

For at least the foregoing reasons, it is believed that the pending claims 1-21 are in proper condition for allowance and an action to such effect is earnestly solicited. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

Date : *March 13, 2006*

Respectfully submitted,

Belinda Lee

Registration No.: 46,863

Jianq Chyun Intellectual Property Office
7th Floor-1, No. 100
Roosevelt Road, Section 2
Taipei, 100
Taiwan
Tel: 011-886-2-2369-2800
Fax: 011-886-2-2369-7233
Email: belinda@jicpgroup.com.tw
Usa@jicpgroup.com.tw